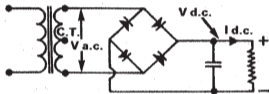


Winding temperature rise within CEE 15 limits.  
 Designed for full rating at +25°C ambient temperature.  
 Flash tested 2000V a.c. minimum.  
 Primaries suitable for 50 to 60Hz operation.  
 All secondary voltages are full load

$$\text{Regulation} = ((\text{Off load voltage} - \text{full load voltage}) / \text{Off load voltage}) \times 100\%$$

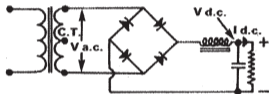
**Note:**

**FULL WAVE BRIDGE – CAPACITIVE INPUT FILTER**



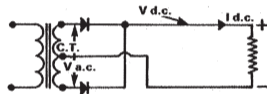
$$\begin{aligned} V_{d.c.} &= 1.41 \times V_{a.c.} \\ I_{d.c.} &= 0.62 \times I_{a.c.} \end{aligned}$$

**FULL WAVE BRIDGE – CHOKE INPUT FILTER**



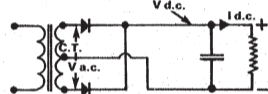
$$\begin{aligned} V_{d.c.} &= 0.90 \times V_{a.c.} \\ I_{d.c.} &= 0.94 \times I_{a.c.} \end{aligned}$$

**FULL WAVE – RESISTIVE LOAD**



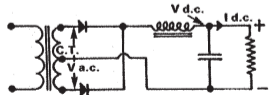
$$\begin{aligned} V_{d.c.} &= 0.45 \times V_{a.c.} \\ I_{d.c.} &= 1.27 \times I_{a.c.} \end{aligned}$$

**FULL WAVE – CAPACITIVE INPUT FILTER**



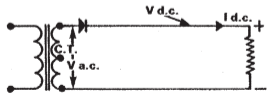
$$\begin{aligned} V_{d.c.} &= 0.71 \times V_{a.c.} \\ I_{d.c.} &= 1.0 \times I_{a.c.} \end{aligned}$$

**FULL WAVE – CHOKE INPUT FILTER**



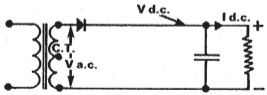
$$\begin{aligned} V_{d.c.} &= 0.45 \times V_{a.c.} \\ I_{d.c.} &= 1.54 \times I_{a.c.} \end{aligned}$$

**HALF WAVE – RESISTIVE LOAD**



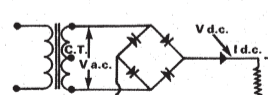
$$\begin{aligned} V_{d.c.} &= 0.45 \times V_{a.c.} \\ I_{d.c.} &= 0.64 \times I_{a.c.} \end{aligned}$$

**HALF WAVE – CAPACITIVE INPUT FILTER**



$$\begin{aligned} V_{d.c.} &= 1.41 \times V_{a.c.} \\ I_{d.c.} &= 0.28 \times I_{a.c.} \end{aligned}$$

**FULL WAVE BRIDGE – RESISTIVE LOAD**



$$\begin{aligned} V_{d.c.} &= 0.90 \times V_{a.c.} \\ I_{d.c.} &= 0.90 \times I_{a.c.} \end{aligned}$$